

## YE UNITED STATES PATENT AND TRADIFMATIK OFFICE

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**Inventor:** 

Thomas D. Fletcher

TECHNOLOGY CENTER 2800

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**Group Art Unit:** 

Examiner:

Title:

Vibol Tan

ZIPPER DOMINO CARRY GENERATE CELL FOR FAST ADDERS

AN 2 2 2003

GROUP 3600

ASSISTANT COMMISSIONER FOR PATENTS Washington, D.C. 20231

## RESPONSE TO OFFICE ACTION

In response to the Office Action mailed on November 14, 2002, Applicants submit the following:

## IN THE CLAIMS:

Please substitute the amended claims below for the pending claims with the same claim numbers. A version with markings to show changes made is provided in Appendix A.

Please cancel claims 2-5 without prejudice.

Please amend the claims 1, 6, 8 and 10 as follows:

1. (Amended) A logic stage comprising:

a precharge circuit connected to a first potential and a differential output defined by a first output node and a second output node;

a first evaluate circuit connected to a second potential and the first output node; and a second evaluate circuit connected to the second potential and the second output node, the second evaluate circuit being symmetric with the first evaluate circuit, each evaluate circuit including a transistor stack connected between the second potential and one of the output nodes,